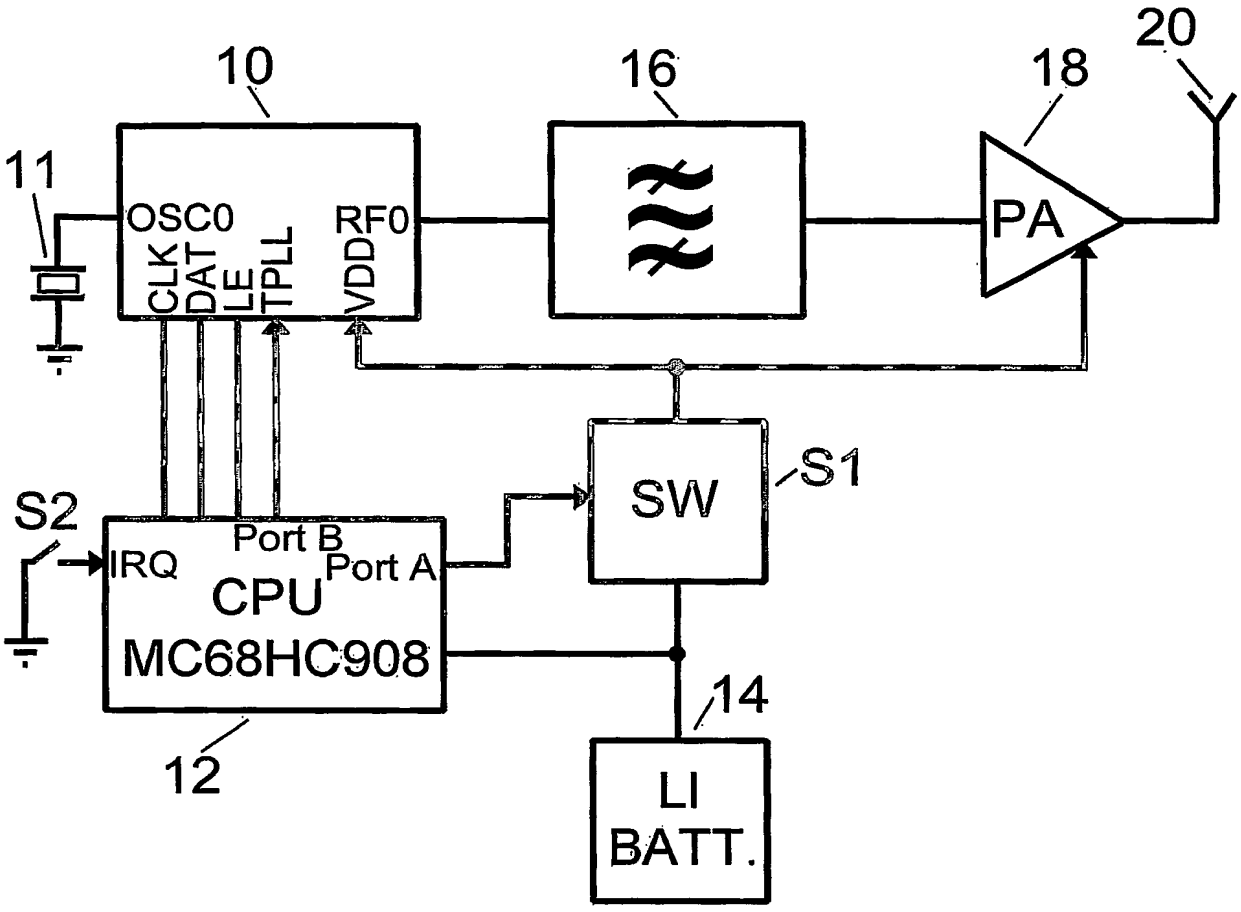
10
16
18
20
11
OSC0
RF0
CLK
DAT
LE
TPLL
VDD
PA
SW
S1
S2
IRQ
Port B
Port A
CPU
MC68HC908
12
14
LI
BATT.

Fig. 1